## Author index of Volume 26 (1996)

(The issue number is given in front of the page numbers)

Achatz, K. and W. Schulte, Massive parallelization of divide-and-conquer	
algorithms over powerlists	(1-3) 59- 78
Apt, K.R., Arrays, bounded quantification and iteration in logic and	
constraint logic programming	(1-3) $133-148$
Back, R.J.R., A.J. Martin and K. Sere, Specifying the Caltech asynchronous	
microprocessor	(1-3) 79- 97
Backhouse, R., Mathematics of program construction	(1-3) 5- 9
Backhouse, R., see H. Doornbos	(1-3) 217-236
Berghammer, R. and B. von Karger, Towards a design calculus for CSP	(1-3) 99-115
Bird, R.S., Functional algorithm design	(1-3) 15- 31
Coquand, T., An algorithm for type-checking dependent types	(1-3) $167-177$
Curtis, S. and G. Lowe, Proofs with graphs	(1-3) 197-216
Desharnais, J., see M. Frappier	(1-3) 237-254
Dielissen, V.J., see A. Kaldewaij	(1-3) 149–165
Doornbos, H. and R. Backhouse, Reductivity	(1-3) 217-236
Frappier, M., A. Mili and J. Desharnais, A relational calculus for program	
construction by parts	(1-3) 237–254
Kaldewaij, A. and V.J. Dielissen, Leaf tress	(1-3) 149–165
Lowe, G., see S. Curtis	(1-3) 197-216
Martin, A.J., see R.J.R. Back	(1-3) 79- 97
Mili, A., see M. Frappier	(1-3) 237–254
Möller, B., Preface	(1-3) 3
Monin, JF., Exceptions considered harmless	(1-3) 179–196
Russling, M., Deriving a class of layer-oriented graph algorithms	(1-3) 117-132
Shankar, N., Steps towards mechanizing program transformations using PVS	(1-3) 33- 57
Schulte, W., see K. Achatz	(1-3) 59- 78
Sere, K., see R.J.R. Back	(1-3) 79- 97
Sintzoff, N., Editorial	(1-3) 1- 2
Stølen, K., Refinement principles supporting the transition from	
asynchronous to synchronous communication	(1-3) 255–272
Turski, W.M., I Was a Computer	(1-3) 11- 14
von Karger B. see R. Berghammer	(1-3) 99-115

